Department of Electronics & Communication Engineering

2025-26

PCB Design Workshop: From Basics to Brilliance

The Department of Electronics and Communication Engineering organized a Five-day workshop titled "PCB Design Workshop: From Basics to Brilliance" for all ECE students on 10-02-2026 to 14-02-2026 at 10:00 AM in the Seminar Hall. The workshop aimed to provide students with practical exposure to Printed Circuit Board (PCB) design, fabrication techniques, and real-time hardware implementation skills essential for electronics engineering.

The primary objective of the workshop was to bridge the gap between theoretical circuit design and practical hardware realization by introducing students to PCB layout design, component placement, etching processes, drilling, soldering techniques, and circuit testing procedures. The sessions emphasized the importance of PCB technology in modern electronic product development and industry-oriented applications.

During the workshop, students were trained in schematic design, PCB layout using relevant software tools, and hands-on fabrication procedures. The resource person demonstrated industry-standard PCB manufacturing practices and explained safety measures, quality control, and troubleshooting techniques involved in circuit development. The hands-on sessions enabled students to design and fabricate basic electronic circuits, thereby improving their practical skills and confidence in hardware implementation.

Students actively participated throughout the workshop and clarified their technical doubts through interactive discussions and problem-solving activities. The workshop significantly enhanced students' understanding of electronics design concepts and strengthened their ability to apply theoretical knowledge in real-time engineering applications.

The program concluded with a vote of thanks. Overall, the workshop was highly informative, practical, and beneficial, contributing to experiential learning and improving the technical competency and industry readiness of ECE students.

Resource Person Details:

P. Srinivasa Karteek
Office Superintendent,
Signal & Telecom Dept,
Secunderabad Division
South Central Railway.

Industrial Tour: KTPS

The Department of Electronics and Communication Engineering (ECE), Priyadarshini Institute of Science and Technology for Women, organized an Industrial Tour to Kothagudem Thermal Power Station (KTPS) on 31 January 2026, as a part of the academic curriculum.

The industrial visit was conducted with the objective of providing practical exposure and enhancing students' understanding of power generation systems, control mechanisms, and industrial safety practices. During the visit, students gained valuable insights into the working principles of thermal power plants, including boiler operations, turbine systems, generators, and control rooms.

The visit was highly informative and helped students bridge the gap between theoretical knowledge and real-time industrial applications. The interaction with industry professionals enriched the learning experience and motivated students toward industry-orientedl earning.

The program was successfully conducted under the guidance and support of the college management and faculty members.

One-Day Online Webinar on "Job Opportunities in the VLSI/Semiconductor Industry"
Date: 11-09-2025

The Department of Electronics and Communication Engineering organized a one-day webinar titled "Job Opportunities in the VLSI/Semiconductor Industry" for III and IV Year ECE students on 11 September 2025 at 10:00 AM in the Seminar Hall.

The primary objective of the webinar was to bridge the gap between academic learning and industry requirements by providing students with exposure to emerging trends and career

opportunities in the VLSI and Semiconductor industry, which is a rapidly expanding core sector in electronics engineering.

The session focused on an overview of the semiconductor ecosystem, industry expectations, and various career roles such as VLSI Design Engineer, Verification Engineer, Physical Design Engineer, and Process Engineer. The resource person emphasized the importance of strong fundamentals in digital electronics, VLSI design concepts, programming skills, and EDA tools for securing employment in core industries. The talk also highlighted the relevance of internships, certifications, and higher studies in enhancing employability.

An interactive question-and-answer session enabled students to clarify their queries regarding skill development, career planning, and industry readiness. The active participation of students reflected their keen interest in pursuing careers in the VLSI/Semiconductor domain.

The webinar successfully met its objectives by enhancing students' awareness of industry trends and motivating them towards focused skill development. The program concluded with a vote of thanks, and overall, the webinar was highly informative, relevant, and beneficial to the participants.

Resource Person Details:

M. Usha Rani
Director
Accurize Technologies Private Limited.
Guntur.

One-Day Seminar on "Emerging Technologies
Date: 20-08-2025

The Department of Electronics and Communication Engineering organized a one-day seminar titled "Emerging Technologies" for III and IV Year ECE students on 20 August 2025 at 10:00 AM in the Seminar Hall.

The primary objective of the seminar was to bridge the gap between academic learning and industry requirements by providing students with exposure to emerging trends and career opportunities in the VLSI, Embedded Systems and Semiconductor industry, which is a rapidly expanding core sector in electronics engineering.

The session focused on an overview of the semiconductor ecosystem, industry expectations, and various career roles such as VLSI Design Engineer, Verification Engineer, Physical Design Engineer, and Process Engineer. The resource person emphasized the importance of strong fundamentals in digital electronics, VLSI design concepts, Embedded Systems , programming skills, and EDA tools for securing employment in core industries. The talk also highlighted the relevance of internships, certifications, and higher studies in enhancing employability.

An interactive question-and-answer session enabled students to clarify their queries regarding skill development, career planning, and industry readiness. The active participation of students reflected their keen interest in pursuing careers in the VLSI, Embedded Systems and Semiconductor domain.

The seminar successfully met its objectives by enhancing students' awareness of industry trends and motivating them towards focused skill development. The program concluded with a vote of thanks, and overall, the seminar was highly informative, relevant, and beneficial to the participants.

Resource Person Details:

Dr. R. Upendar Rao
Director
BIST Technologies Private Limited.
Vijayawada.

Event Photo: